

WHAT IS CLAIMED IS:

1. An image display device formed on an insulation substrate, comprising:

an image display panel including a plurality of pixel display circuits arranged in a plurality of rows and a plurality of columns, a plurality of scan lines provided in correspondence with said plurality of rows, respectively, and a plurality of data lines provided in correspondence with said plurality of columns, respectively;

a plurality of transistors having first electrodes connected to said plurality of data lines, respectively, and made nonconductive when said image display panel is in normal operation;

a first inspection terminal connected to second electrodes of the odd-numbered transistors among said plurality of transistors;

a second inspection terminal connected to second electrodes of the even-numbered transistors among said plurality of transistors; and

a first control terminal connected to gates of said plurality of transistors, and receiving a control signal for controlling said plurality of transistors at inspection of said image display panel.

2. The image display device according to claim 1, further comprising:

a plurality of data terminals provided in correspondence with said plurality of data lines, respectively, and each receiving a pixel potential for displaying a pixel on said pixel display circuit at said normal operation, wherein

each of said first inspection terminal, said second inspection terminal and said first control terminal is larger in size than said data terminal.

3. The image display device according to claim 1, further comprising:

a plurality of data terminals provided in correspondence with said plurality of data lines, respectively, and each receiving a pixel potential for

5 displaying a pixel on said pixel display circuit at said normal operation,
wherein

said plurality of data terminals are arranged at a predetermined
pitch, and

10 said first inspection terminal, said second inspection terminal and
said first control terminal are arranged at a wider pitch than the pitch of
said plurality of data terminals.

4. The image display device according to claim 1, wherein
each of said first inspection terminal, said second inspection terminal
and said first control terminal receives a predetermined potential for
making said plurality of transistors nonconductive at said normal operation.

5. The image display device according to claim 4, wherein
each of said first inspection terminal, said second inspection terminal
and said first control terminal is arranged in a region where a
semiconductor chip is mounted after said inspection, and receives said
5 predetermined potential from the semiconductor chip mounted at said
normal operation.

6. The image display device according to claim 1, further
comprising:

5 three pads connected to said first inspection terminal, said second
inspection terminal and said first control terminal, respectively, arranged
in a region where a semiconductor chip is mounted after said inspection,
and receiving a predetermined potential for making said plurality of
transistors nonconductive from the mounted semiconductor chip at said
normal operation.

7. The image display device according to claim 1, wherein
a plurality of module regions are provided on said insulation
substrate,

5 said image display panel, said plurality of transistors, said first
inspection terminal, said second inspection terminal and said first control

terminal are formed in each of the module regions,
said image display device further comprises:

a first common terminal formed outside said plurality of module regions, and connected to said plurality of first inspection terminals;

10 a second common terminal formed outside said plurality of module regions, and connected to said plurality of second inspection terminals; and

a plurality of second control terminals provided in correspondence with said plurality of first control terminals, respectively, and formed outside said plurality of module regions, each second control terminal
15 receiving a control signal and applying the control signal to the corresponding first control terminal at said normal operation, and

each of the module regions is separated from said first common terminal, said second common terminal and said plurality of second control terminals after said inspection.

8. The image display device according to claim 1, wherein
a plurality of module regions are provided on said insulation substrate,

5 said image display panel is formed in each of the module regions, said plurality of transistors and said first control terminal are provided in correspondence with each of the image display panels, and formed outside said plurality of module regions,

said first and second inspection terminals are provided to be common to said plurality of image display panels, and formed outside said plurality
10 of module regions, and

each of the module regions is separated from a plurality of sets of said plurality of transistors, said plurality of first control terminals, said first inspection terminal and said second inspection terminal after said inspection.

9. The image display device according to claim 1, wherein
said image display panel is separated from said plurality of transistors, said first inspection terminal, said second inspection terminal and said first control terminal after said inspection.